

Revision History

Version 1.0 Released on September 5, 2008

Version 1.1 Released on September 10, 2008

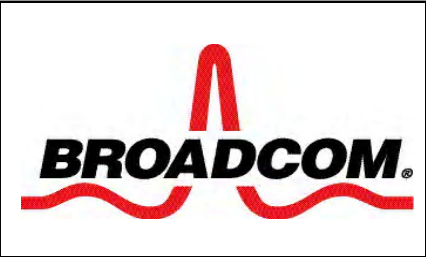
Changed the component footprints to match the layout.

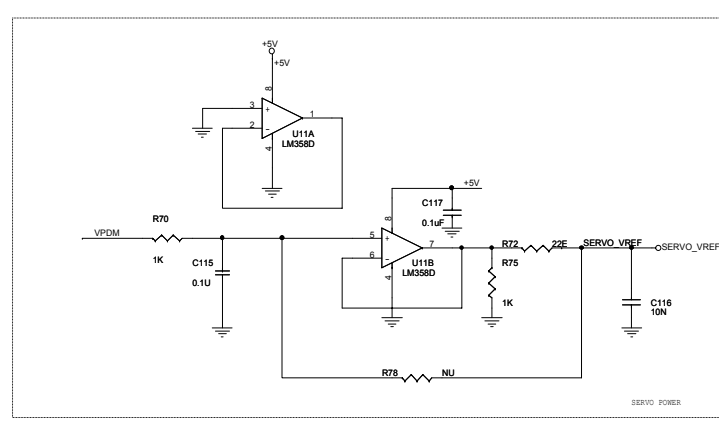
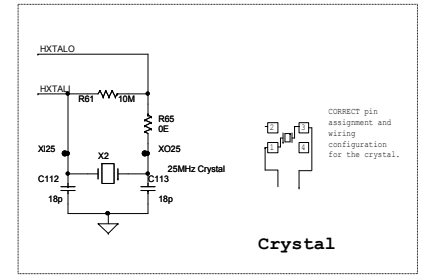
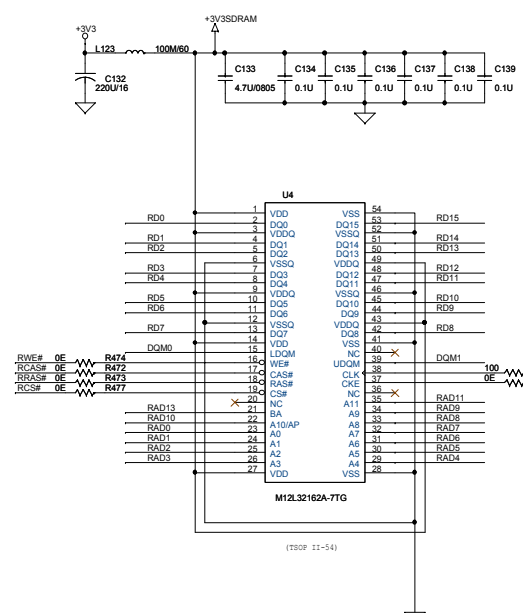
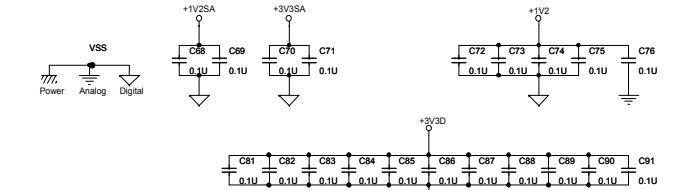
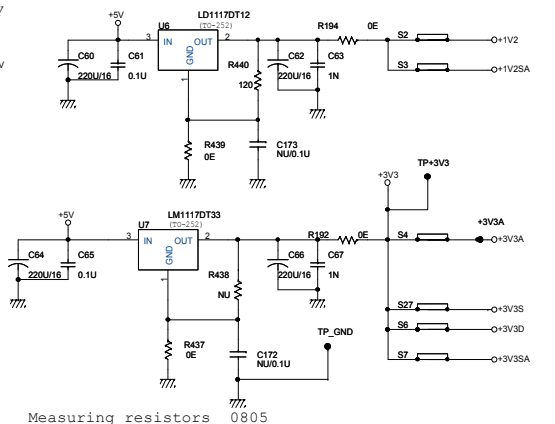
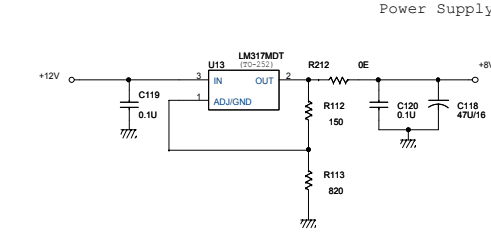
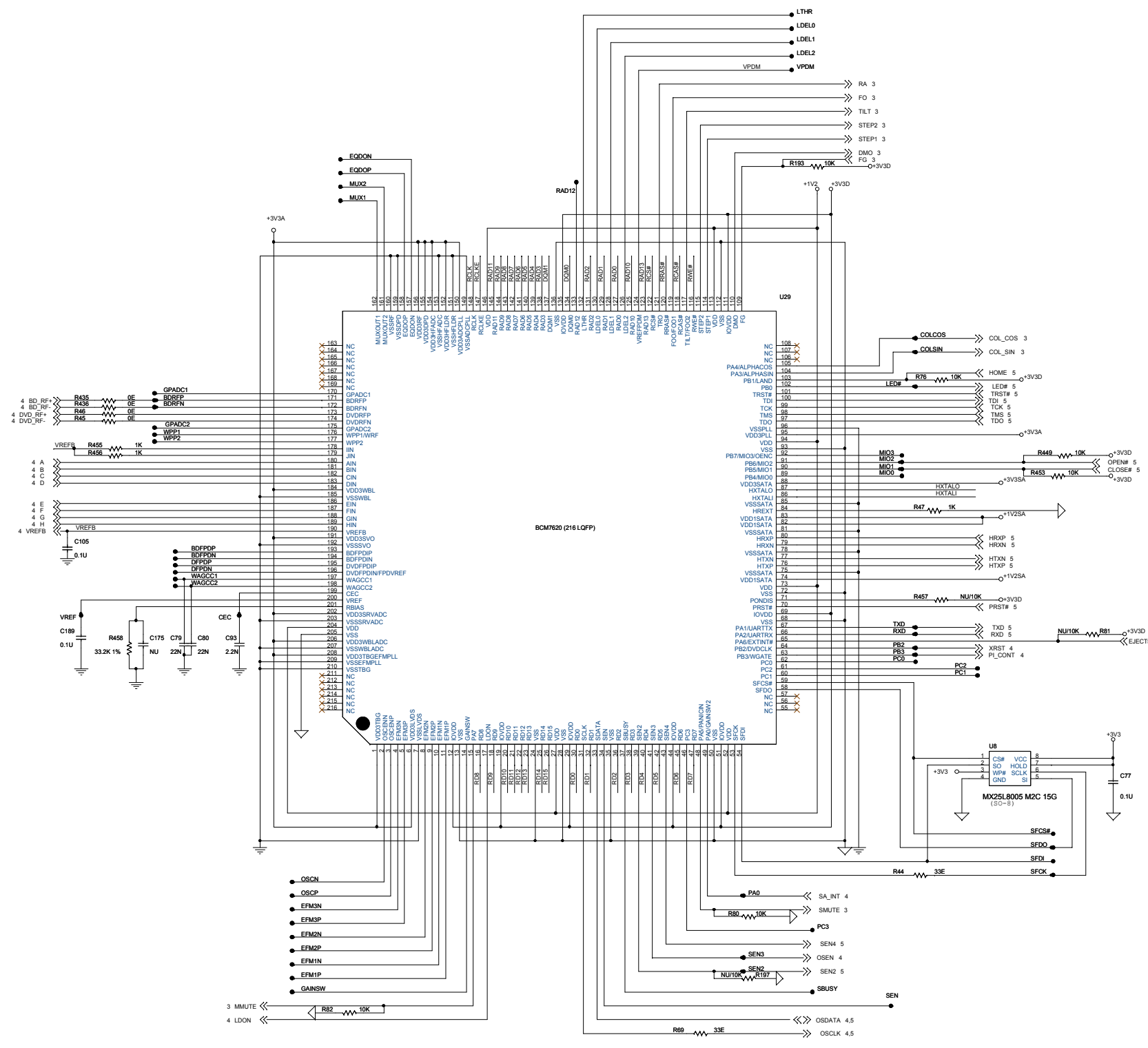
Version 1.2 Released on September 12, 2008

- Changed the J1 and J2 component footprints to match the layout.
- Changed the S2, S3, S4, S6, S7, S27component footprints to make shorted resistors.
- Moved OPEN, CLOSE Testpoints onto OPEN, CLOSE signal traces.

Version 1.3 Released on September 12, 2008

Changed SA_INT and PI_CONT testpoints to match signal.

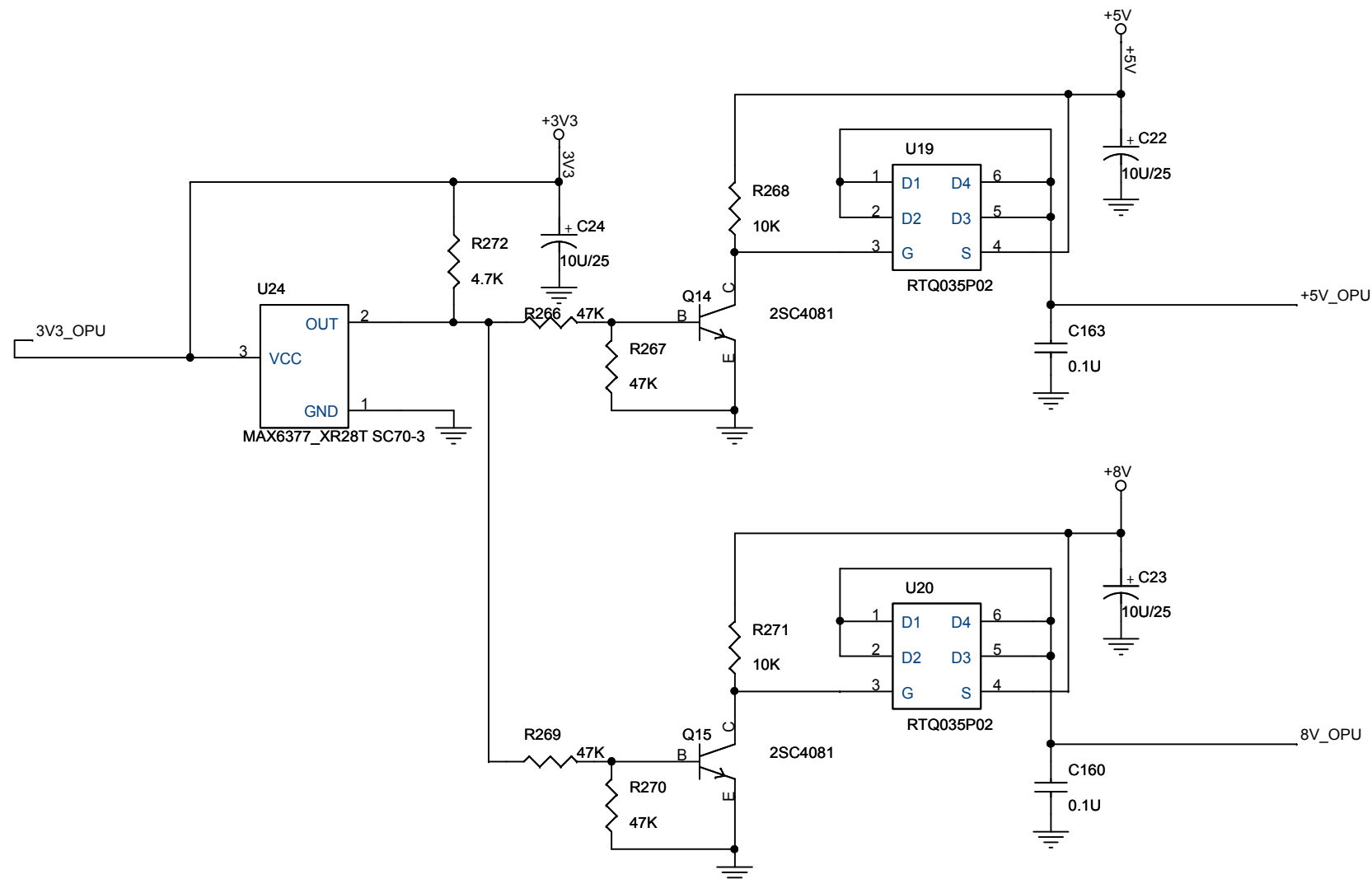
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	BCM97620R01			
	Size		DWG NO	Rev
	B		?	1.3
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BOTTOM

Pin	Signal	Direction	Signal Type
1	FD1	S8	FOC+
2	FR1	S9	FOC-
3	TD	S10	RAD+
4	TR	S11	RAD-
5	FR2	S12	TILT-
6	FD2	S13	TILT+
7	SA-A-	S14	ColAn
8	SA-B-	S15	ColBn
9	SA-A+	S16	ColA
10	SA-B+	S17	ColB
11			SA_INT
12			PI_CONT
13			
14	BD_RF+		BD_RF+ 2
15	BD_RF-		BD_RF- 2
16			
17	DVD_RF+		DVD_RF+ 2
18	DVD_RF-		DVD_RF- 2
19			
20			+5V_OPU
21	S18	A	A 2
22	S19	B	B 2
23	S20	C	C 2
24	S21	D	D 2
25	VC-PD	S22	VREFB 2
26	S23	F	F 2
27	S24	E	E 2
28	S25	G	G 2
29	S26	H	H 2
30			
31			
32			
33			
34			
35			
36			3V3_OPU
37			+5V_OPU
38			8V_OPU
39	R536	O_37	OSCLK 2,5
40			OSCLK 2
41			OSDATA 2,5
42	ORST	LDON	XRST 2
43			LDON 2
44	O_43		
45	O_44		
46	O_45		

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H	●	H	VREFB	●	VREFB
A	●	A	BD_RF+	●	RFP
C	●	C	BD_RF-	●	RFN
G	●	G	DVD_RF+	●	DRFP
E	●	E	DVD_RF-	●	DRFN
B	●	B	OSCLK	●	OSCLK
D	●	D	OSDATA	●	OSDATA
F	●	F	OSEN	●	OSEN
LDON	●	LDON	ORST	●	ORST
			PI_CONT	●	PI_C
			SA_INT	●	SA_I



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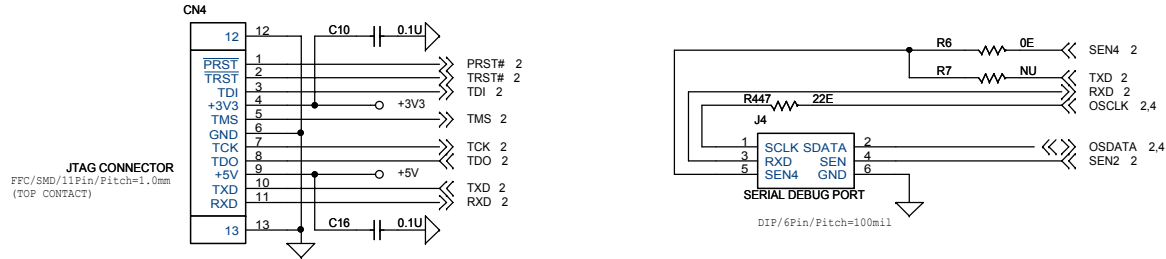
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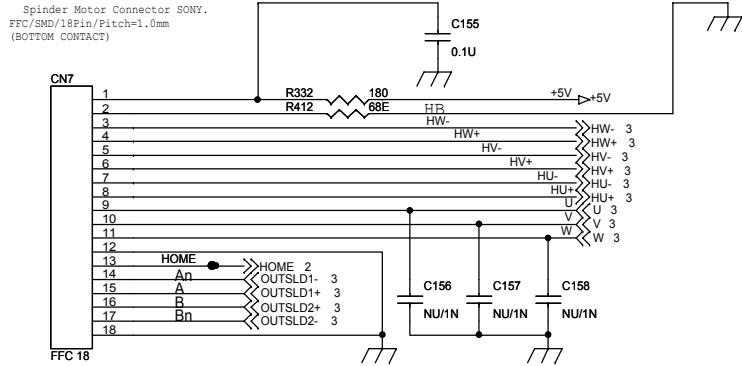
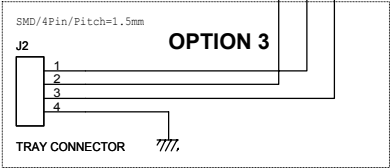
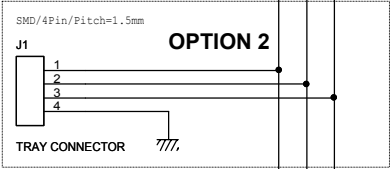
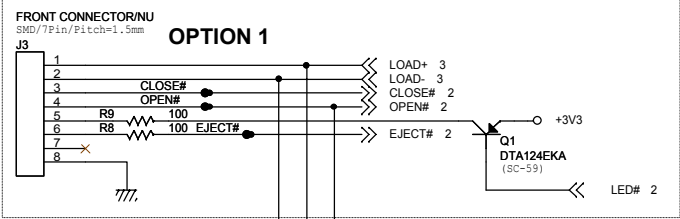
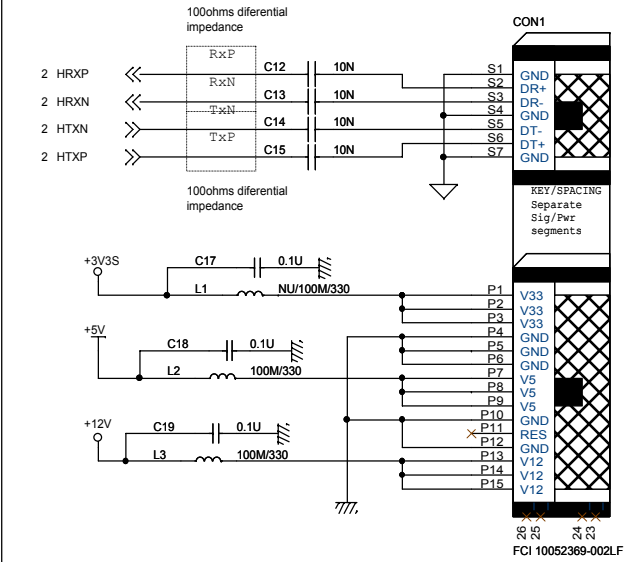
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SATA - CONNECTOR



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